

Form PTO-1449		U.S. Dept. of Commerce Patent & Trademark Office		Atty. Docket: 02-ZAV-220		Serial No. 10/718,257	
List of Documents Cited by Applicant (Use several sheets if necessary)				Applicant: Jan Frans Lucien CRANINCKX			
				Filing Date: November 20, 2003		Group: 2682	
U.S. PATENT DOCUMENTS							
Exis. In'l.		Document Number	Date	Name	Class	Sub- class	Filing Date, if applicable
PK	AA1	6,181,213	January 30, 2001	Chang			
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Sub- class	Trans'l'n Yes/No
PK	AA2	WO 02/27938	April 4, 2002	PCT			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
PK	AA3	Chan-Hong Park et al., "A 1.8-Ghz Self-Calibrated Phase-Locked Loop with Precise I/Q Matching", IEEE Journal of Solid-State Circuits, Vol. 36, No. 5, May 2001, pp. 777-783, XP002241877.					
PK	AA4	S. Khurshed Enam et al., "NMOS IC's for Clock and Data Regeneration in Gigabit-per-Second Optical-Fiber Receivers", IEEE Journal of Solid-State Circuits, Vol. 27, No. 12, December 1992, pp. 1763-1774, XP000329026.					
PK	AA5	European Search Report dated May 20, 2003 for European Application No. 02-447227.6.					
Examiner: <i>/Pankaj Kumar/</i>			Date Considered: 12/25/2006				
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

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